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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
INFORMATION DISCLOSURE STATEMENT		Docket Number: 2885/31	
		JAN 09 2002 Technology Center 2100	
Application Number 09/598,926	Filing Date June 21, 2000	Examiner	Art Unit 2782
Invention Title METHOD OF REPAIRING INTERGRATED CIRCUITS		Inventor(s) Vorbach, et al.	

Assistant Commissioner for Patents
Washington D.C. 20231
Box Patent Application

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
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Date:

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11/09/01

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant hereby bring the references listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
- III. It is believed that no fees are due in connection with this Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account Number 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated: 11/09/01

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